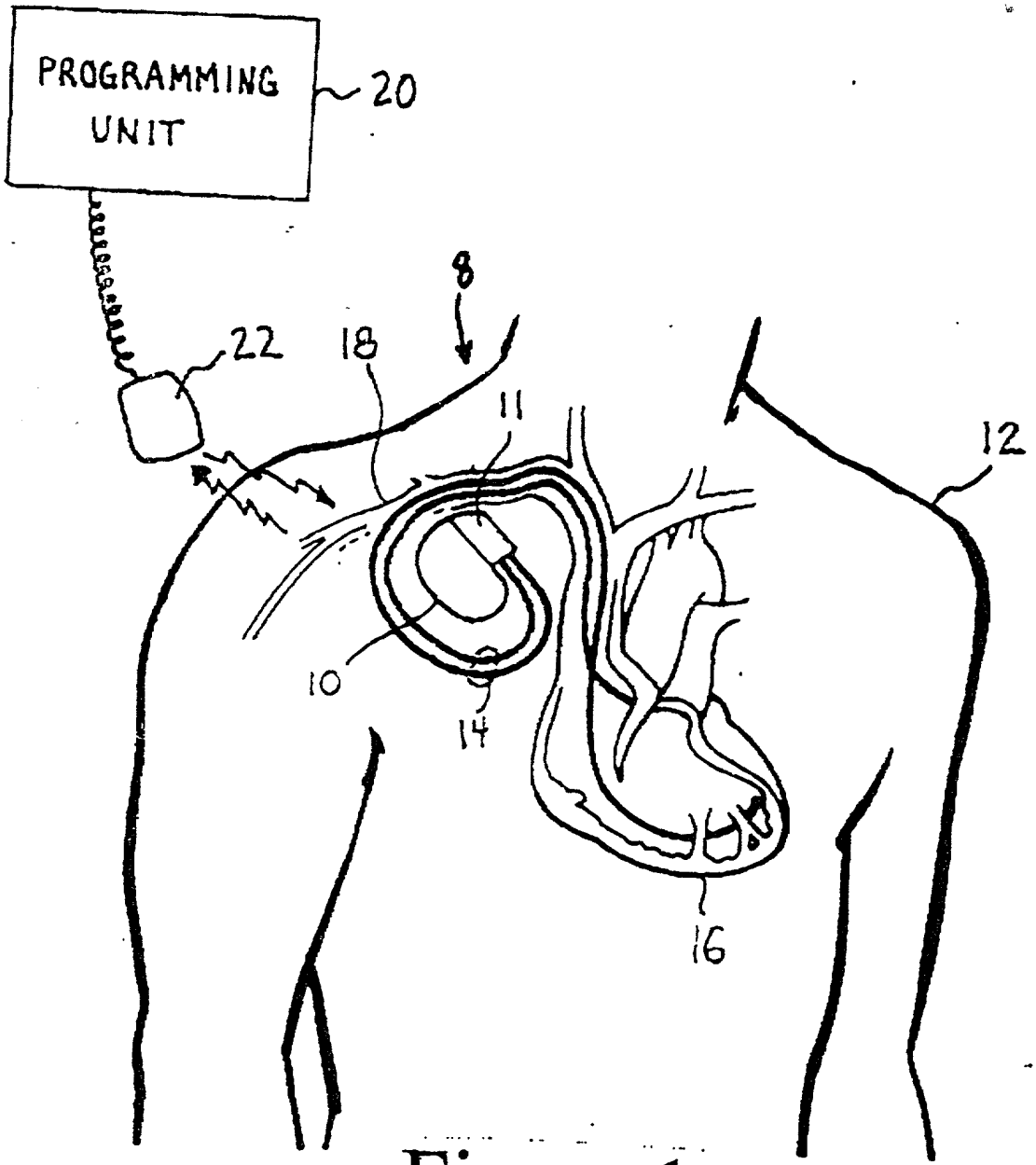


Figure 1

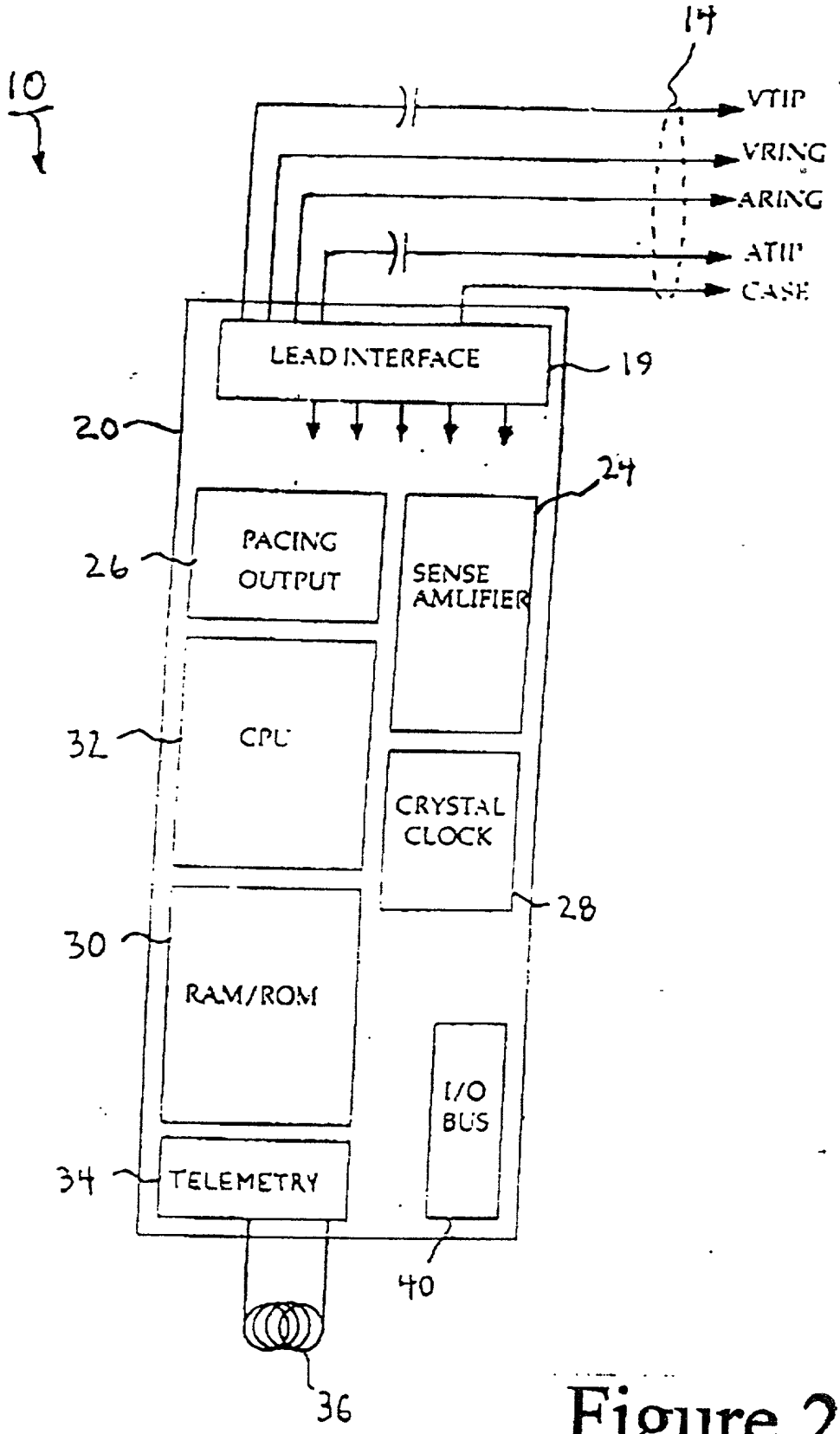

10
14
VTIP
VRING
ARING
ATIP
CASE
LEAD INTERFACE
19
20
24
26
PACING OUTPUT
SENSE AMLIFIER
32
CPU
CRYSTAL CLOCK
28
30
RAM/ROM
I/O BUS
34
TELEMETRY
40
36

Figure 2

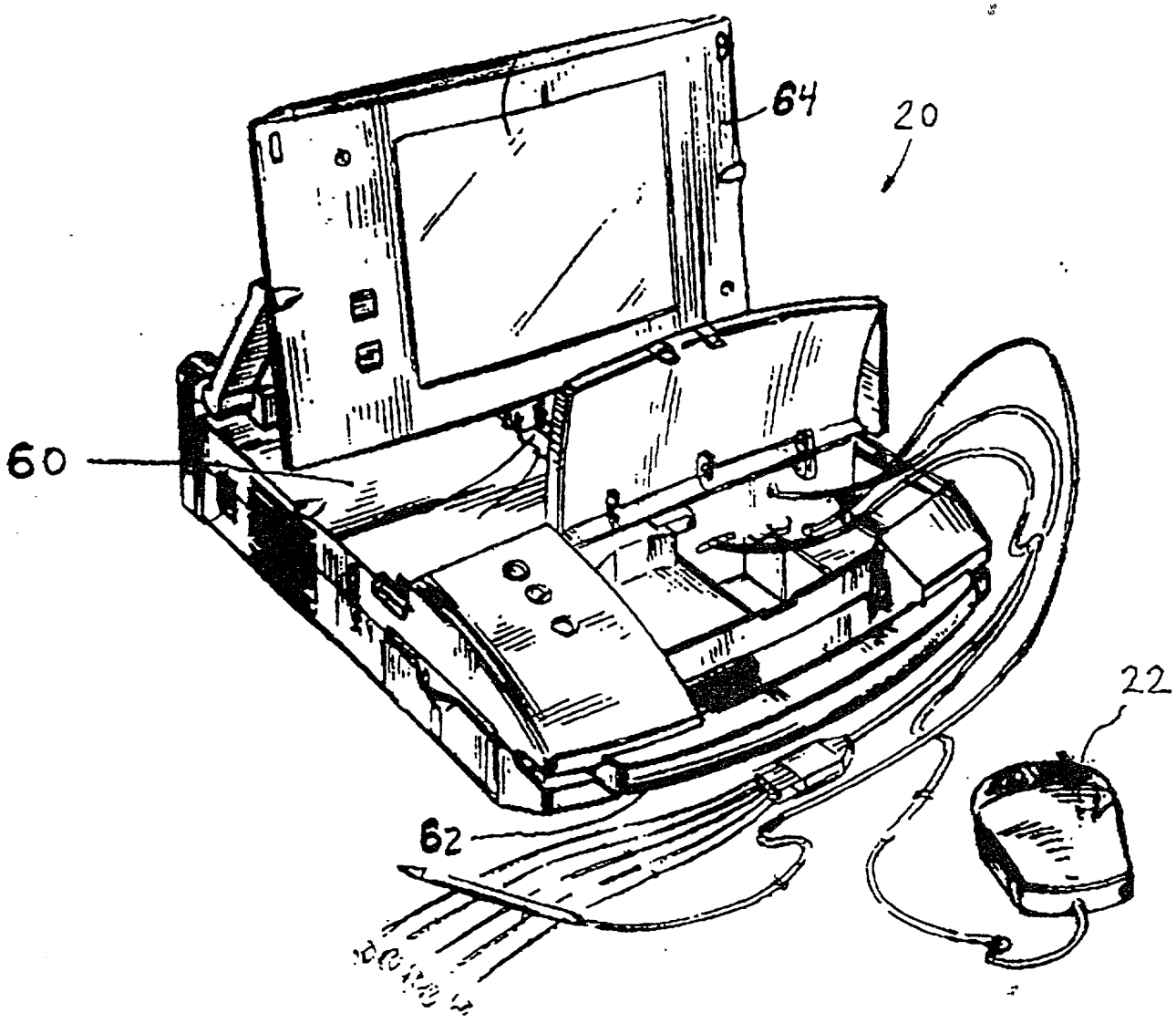
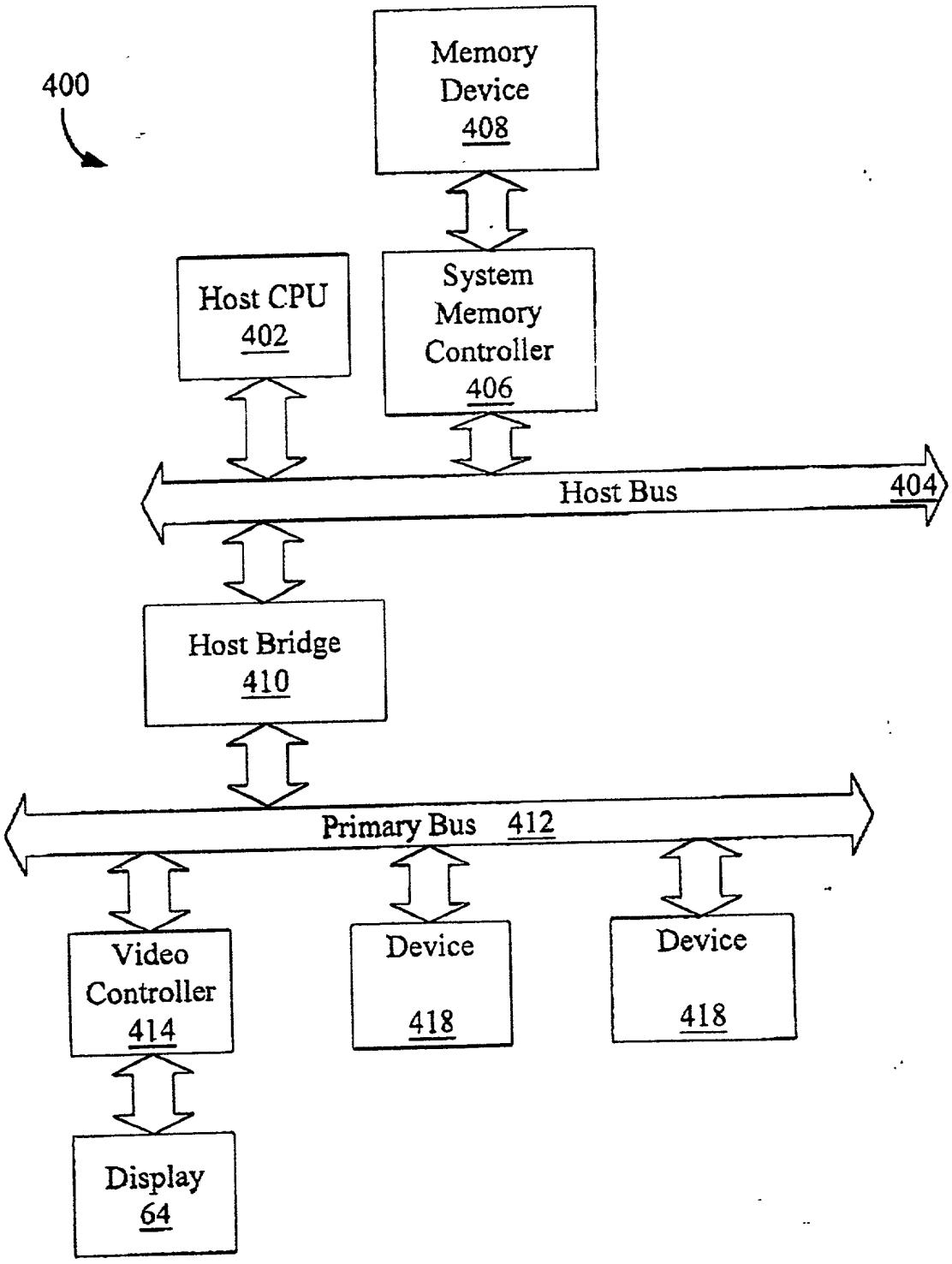


Figure 3

Figure 4


400
Memory Device 408
Host CPU 402
System Memory Controller 406
Host Bus 404
Host Bridge 410
Primary Bus 412
Video Controller 414
Device 418
Device 418
Display 64

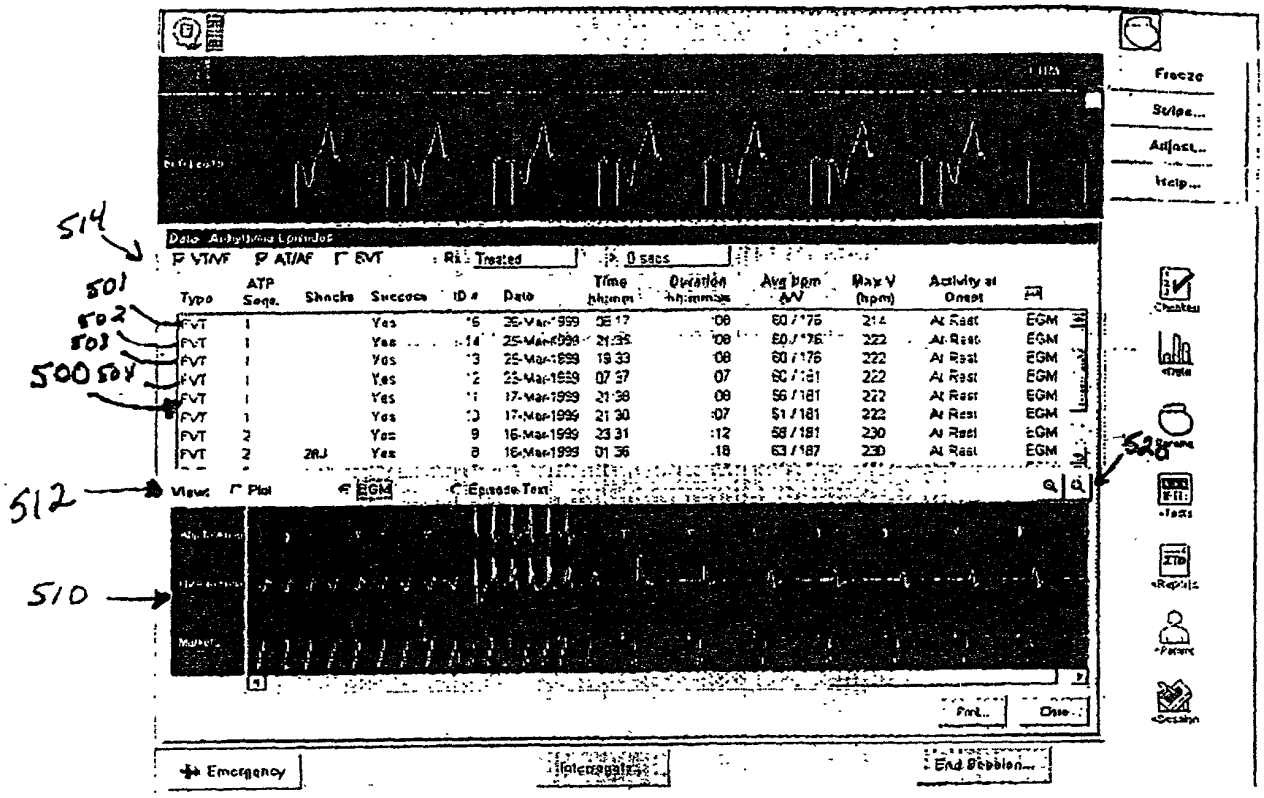

514
501
502
503
500
504
512
510
520
VT/VF
AT/AF
EVT
Trigger
Tested
0 secs
Type
ATP Seqs.
Shocks
Success
ID #
Date
Time
Duration
Avg bpm
Max V
Activity at Onset
EGM
Episode Text
Freeze
Stripe...
Adjust...
Help...
Emergency
End Session...

Figure 5

CRM1 Combined Brady/Tachy Product

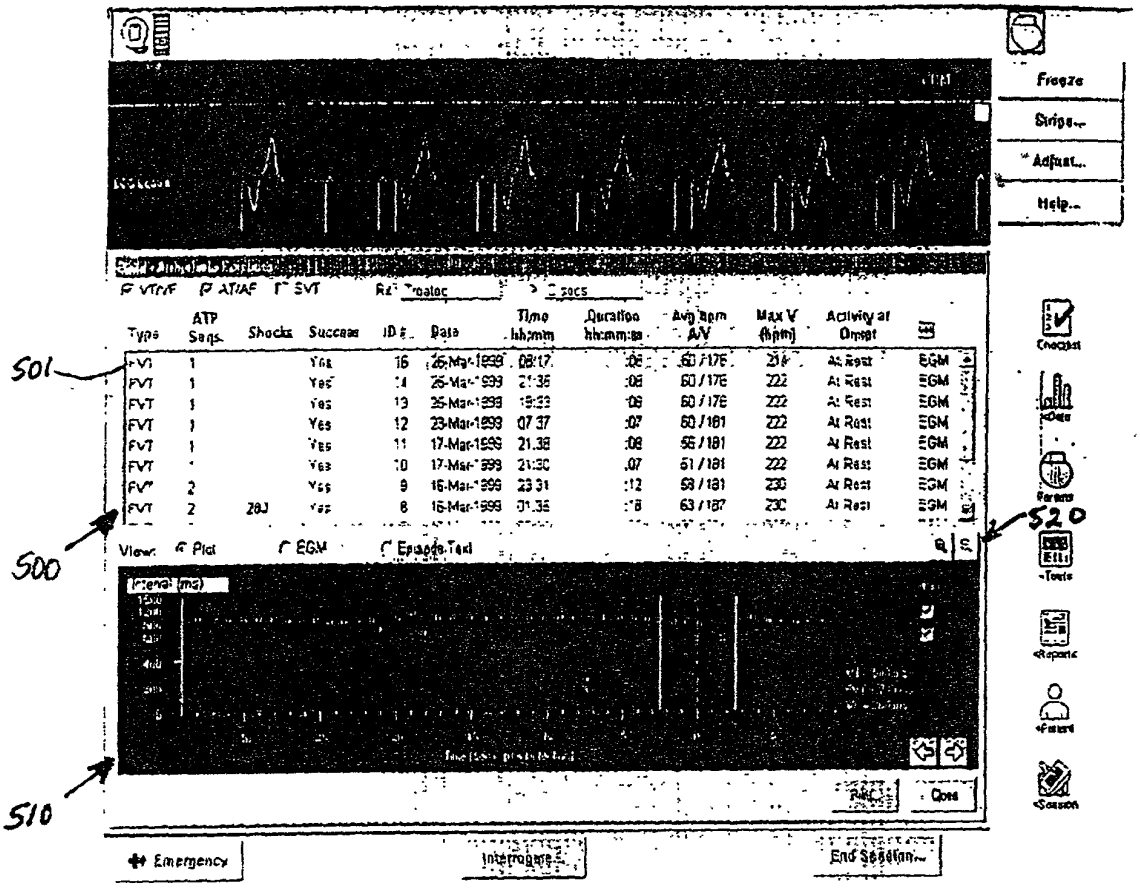


Figure 6

Figure 7

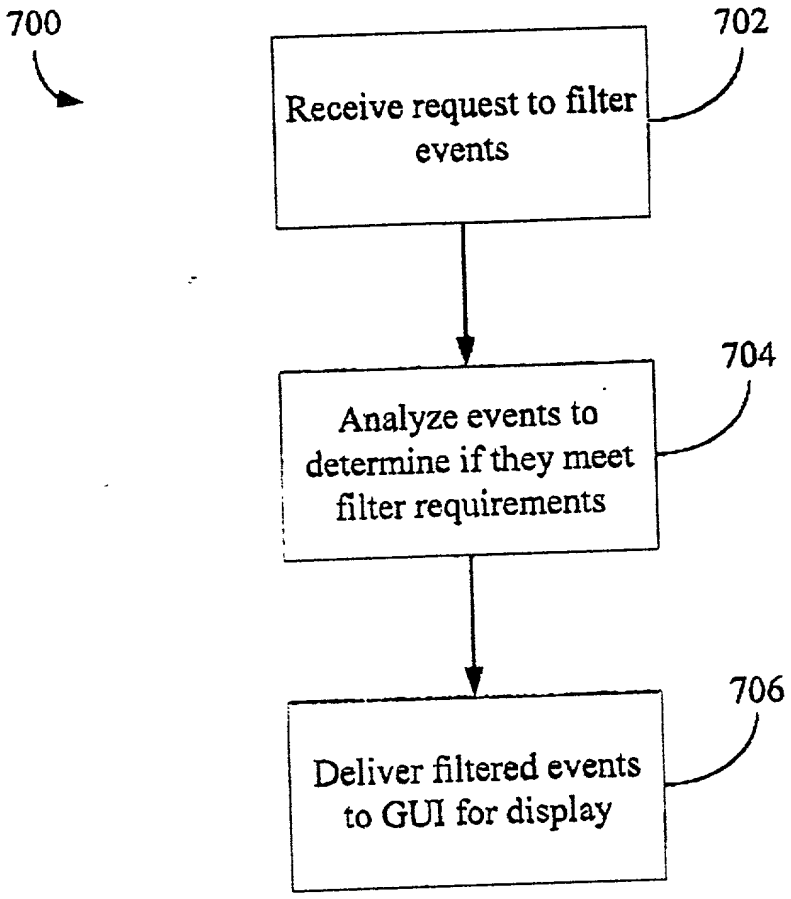
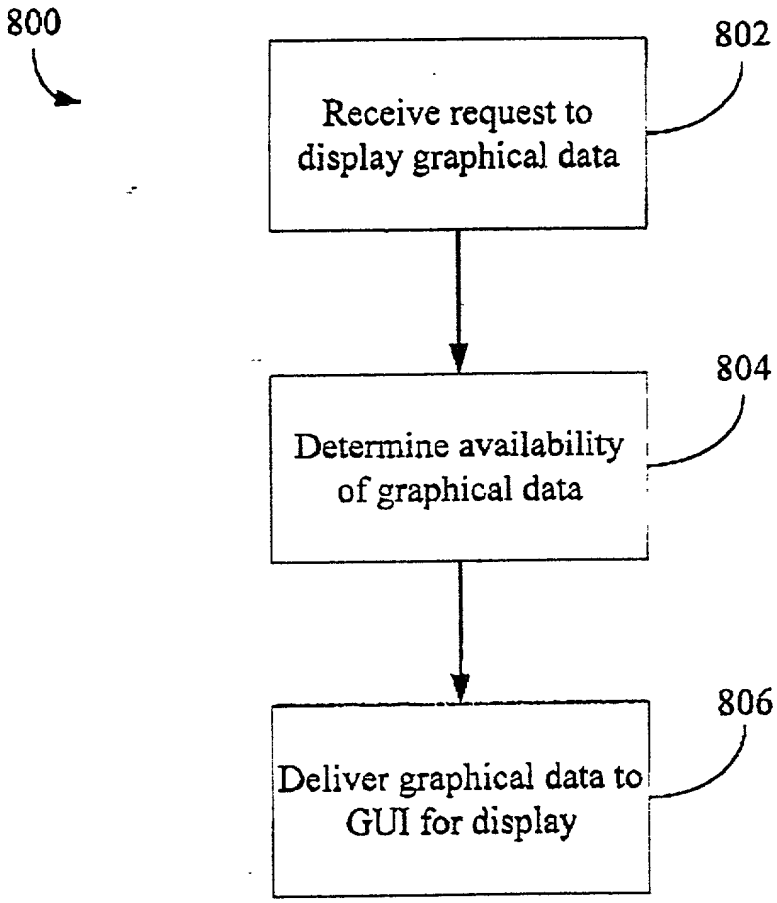
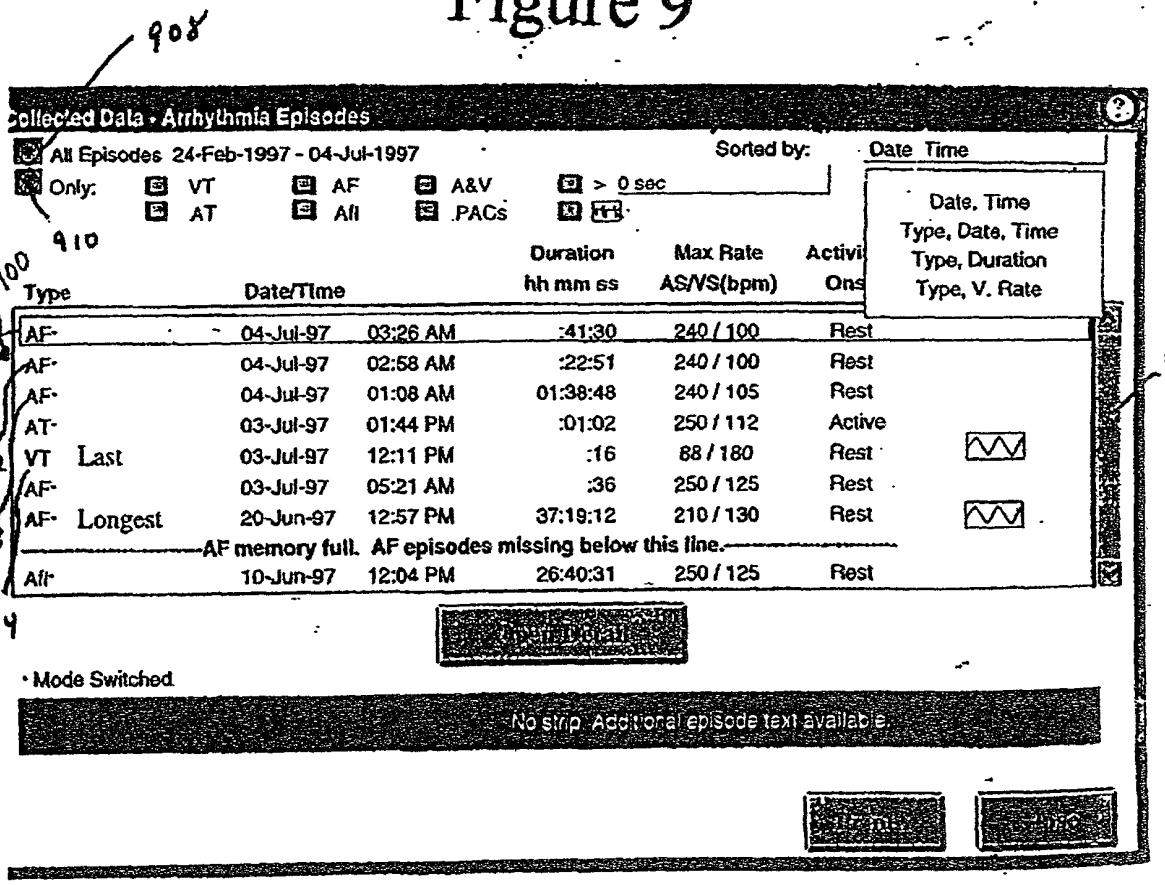

700
702
Receive request to filter events
704
Analyze events to determine if they meet filter requirements
706
Deliver filtered events to GUI for display

Figure 8


800
802
Receive request to display graphical data
804
Determine availability of graphical data
806
Deliver graphical data to GUI for display

Screen 76: Arrhythmia Episode Log - Sorted by: Value Field

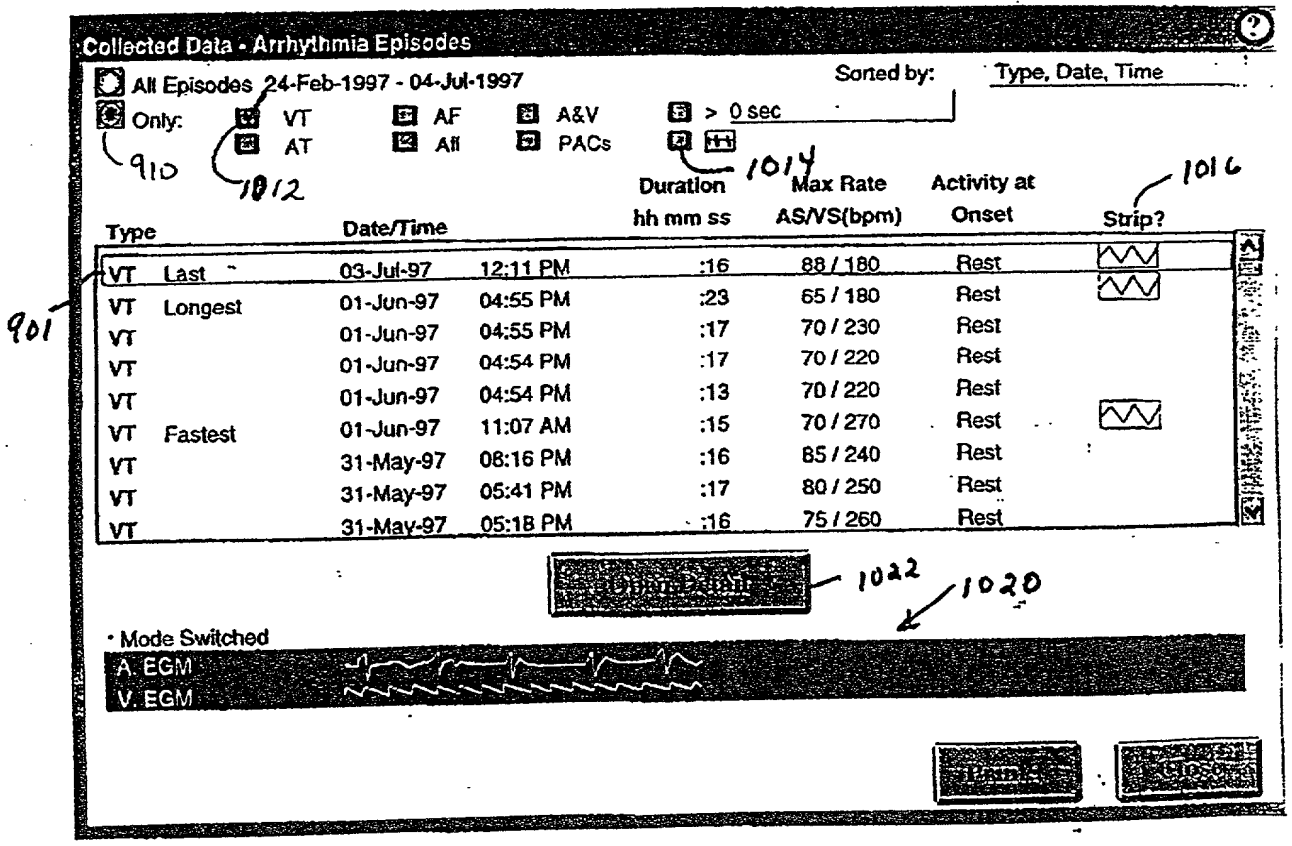
Figure 9



$Header: /projects/sig/repository/devdocs/uidd/aryepis.ai.mif,v 1.35 2000/01/10 17:36:27 cassel Exp $

Screen 77: Arrhythmia Episode Log - sorted by Type, Date, Time

Figure 10



0037455.0007

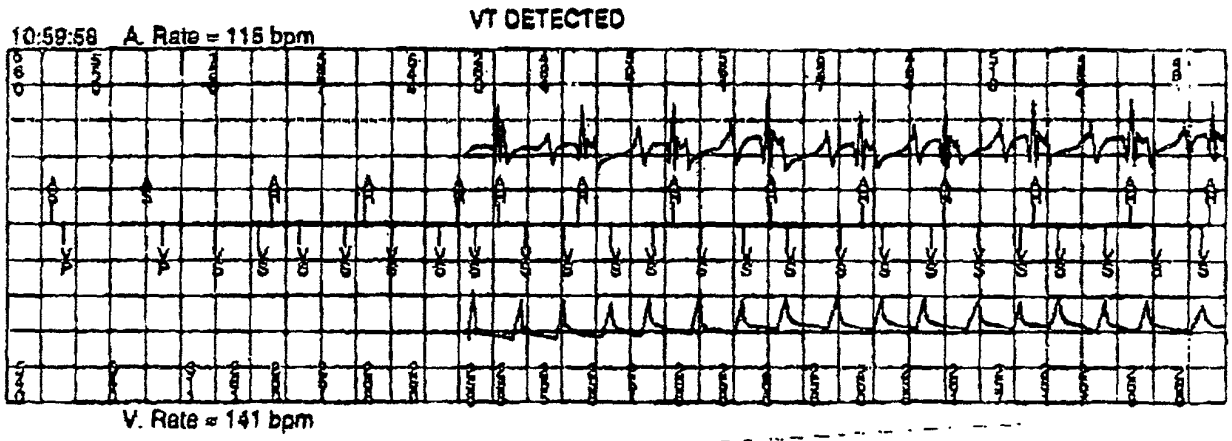


Figure 11